



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
 UNITED STATES PATENT AND TRADEMARK OFFICE
 WASHINGTON, D.C. 20231
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Bib Data Sheet

CONFIRMATION NO. 6368

SERIAL NUMBER 09/737,225	FILING DATE 12/14/2000 RULE	CLASS 365 714	GROUP ART UNIT 2818 2133	ATTORNEY DOCKET NO. BUR920000192US1
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APPLICANTS

R. Dean Adams, St. George, VT;
 Michael R. Ouellette, Westford, VT;
 Jeremy P. Rowland, South Burlington, VT;

**** CONTINUING DATA *********** FOREIGN APPLICATIONS *********IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

** 01/25/2001

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after
Verified and Acknowledged	<i>Guy Lamane 4/4/03</i> Examiner's Signature Initials
STATE OR COUNTRY	VT
SHEETS DRAWING	2
TOTAL CLAIMS	1
INDEPENDENT CLAIMS	1

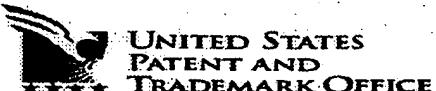
ADDRESS

24241

TITLE

Method and apparatus for testing memory

FILING FEE RECEIVED 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	VT	2	1	1

ADDRESS

IBM Microelectronics
Intellectual Property Law
1000 River Street
972 E
Essex Junction , VT 05452

TITLE

Method and apparatus for testing memory

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